

Attorney Docket No.: 2070.007100/P7001

DECLARATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or the below named inventors are the original, first and joint inventors (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled **UTILIZING SLOW ASIC LOGIC BIST TO PRESERVE TIMING INTEGRITY ACROSS TIMING DOMAINS**, the Specification of which is attached hereto.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims.

I acknowledge the duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim priority benefits under Title 35, United States Code, § 119 and/or § 365 of any foreign application(s) for patent or inventor's certificate, PCT international application(s), and United States provisional application(s), listed below and have also identified below any foreign application for patent or inventor's certificate, PCT international application, or United States provisional application, having a filing date before that of the application on which priority is claimed:

PRIORITY APPLICATION(S)			Priority Claimed
(Number)	(Country)	(Date Filed)	Yes/No
(Number)	(Country)	(Date Filed)	Yes/No

I hereby claim the benefit under Title 35, United States Code, § 120 and/or § 365 of any United States application(s) and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose all information known to me to be material to patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations, § 1.56, which becomes available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
(Application Serial No.)	(Filing Date)	(Status)

I hereby direct that all correspondence and telephone calls be addressed to Jeffrey A. Pyle, Williams, Morgan & Amerson, P.C., 7676 Hillmont, Suite 250, Houston, Texas 77040, (713) 934-4053.

09976333 101201

I HEREBY DECLARE THAT ALL STATEMENTS MADE OF MY OWN KNOWLEDGE ARE TRUE AND THAT ALL STATEMENTS MADE ON INFORMATION AND BELIEF ARE BELIEVED TO BE TRUE; AND FURTHER THAT THESE STATEMENTS WERE MADE WITH THE KNOWLEDGE THAT WILLFUL FALSE STATEMENTS AND THE LIKE SO MADE ARE PUNISHABLE BY FINE OR IMPRISONMENT, OR BOTH, UNDER SECTION 1001 OF TITLE 18 OF THE UNITED STATES CODE AND THAT SUCH WILLFUL FALSE STATEMENTS MAY JEOPARDIZE THE VALIDITY OF THE APPLICATION OR ANY PATENT ISSUED THEREON.

Inventor's Full Name:	MICHAEL	C.	DORSEY
Inventor's Signature:	<i>Michael C. Dorsey</i>		
Country of Citizenship:	USA	Date:	<i>Oct 10, 2001</i>
Residence Address: (street, number, city, state, and/or country)	9450-B Mira Mesa Blvd. #350 San Diego, CA 92126		
Post Office Address: (if different from above)	same as above		

0996563-101201

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MICHAEL C. DORSEY

Serial No.:

Filed: Concurrently Herewith

For: UTILIZING SLOW ASIC LOGIC BIST TO
PRESERVE TIMING INTEGRITY ACROSS
TIMING DOMAINS

§
§
§ Examiner: Unknown
§
§ Group Art Unit: Unknown
§
§ Atty. Docket: 2070.007100/JAP
§ P7001
§
§

POWER OF ATTORNEY

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

The undersigned, being the inventor named in the above-identified application, hereby revokes any previous Powers of Attorney and appoints:

Kenneth Olsen, Reg. No. 26,493; Timothy J. Crean, Reg. No. 37,116; Alexander E. Silverman, Reg. No. 37,940; Anirama R. Gupta, Reg. No. 38,275; Sean P. Lewis, Reg. No. 42,798; Michael J. Schallop, Reg. No. 44,319; Noreen A. Krall, Reg. No. 39,734; Bernice B. Chen, Reg. No. 42,403; Marc D. Foodman, Reg. No. 34,110; Monica D. Lee, Reg. No. 40,696; Pavel Pogodin, Reg. No. 48,205; Naren Chaganti, Reg. No. 44,602; Elaine K. Lee, Reg. No. 41,936; Hugh H. Matsubayashi, Reg. No. 43,779; Paul D. Sorkin, Reg. No. 39,039; Marilyn E. Glaubensklea, Reg. No. 35,521; Ramin Aghevli, Reg. No. 43,462; Andrew C. Chen, Reg. No. 43,544 and Jeffrey L. Myers, Reg. No. 44,252 of SUN MICROSYSTEMS, INC.; and

Danny L. Williams, Reg. No. 31,892; Terry D. Morgan, Reg. No. 31,181; J. Mike Amerson, Reg. No. 35,426; Kenneth D. Goodman, Reg. No. 30,460; Jeffrey A. Pyle, Reg. No. 34,904; Randall C. Furlong, Reg. No. 35,144; Bradley A. Misley, Reg. No. 46,937; Ruben S. Bains, Reg. No. 46,532; Scott F. Diring, Reg. No. 35,119; George J. Oehling, Reg. No. 40,471; Shelley P.M. Fussey, Reg. No. 39,458; Mark D. Moore, Reg. No. 42,903; Louis H. Iselin, Reg. No. 42,684; Raymund F. Eich, Reg. No. 42,508; Thomas H. Belvin, Jr., Reg. No. 43,491; Daren C. Davis, Reg. No. 38,425; and Stephanie A. Wardwell, Reg. No. 48,025 of Williams, Morgan & Amerson, P.C.,

as its attorney or agent so long as they remain with such firms, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one

09976593-101201
TOTAL 101201